

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)

50944.9900

Application Number

Not Yet Assigned

Applicant(s)

Larry Baoqi Li

Filing Date

Filed Herewith

Group Art Unit

NYA

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
jad		5,781,389	7/14/1998	Fukazako et al.	361	56	
jad		5,969,929	10/19/1999	Kleveland et al.	361	111	
jad		5,173,755	12/22/1992	Co et al.	257	361	
jad		5,838,146	11/17/1998	Singer	323	270	
jad		5,631,793	5/20/1997	Ker et al.	361	56	
jad		5,936,284	8/10/1999	Ravanelli	257	361	

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
jad		EP 0 435 047 A2	7/3/1991	Eurpoe	H01L	27/02		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

jad		Chatterjee, A Low Voltage Triggering SCR for ON-Chip ESD Protection at Output and Input Pads, IEEE Electron Device Letters, Vol. 12, No. 1, January 1991, pages 21-22
jad		Duvvury, Rountree, & Adams, Internal Chip ESD Phenomena Beyond the Protection Circuit, IEEE Transactions on Electron Devices, Vol. 35, No. 12, December 1988, pages 2133-2138

EXAMINER

DATE CONSIDERED

9/5/02

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.